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EXPEDITED PROCEDURE – EXAMINING GROUP 2113

S/N 10/628,726

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Suresh Marisetty et al.	Examiner:	Michael C. Maskulinski
Serial No.:	10/628,726	Group Art Unit:	2113
Filed:	July 28, 2003	Docket No.:	884.108US2
Title:	ERROR CORRECTION APPARATUS, SYSTEMS, AND METHODS		
Assignee:	Intel Corporation	Customer Number: 21186	

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In response to the Final Office Action mailed July 6, 2005, please amend the Application as follows, and consider the appended remarks.